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APPLICANTS Hiroshi Inagaki, Kanagawa, JAPAN; Masahiro Shibata, Kanagawa, JAPAN; Shigeki Kawashima, Kanagawa, JAPAN; Nobuyuki Fukuda, Kanagawa, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP05/01937 02/09/2004 ** FOREIGN APPLICATIONS ***** JAPAN 2004-032670 02/09/2004				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 03/30/2007				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>Salina</i> Allowance Examiner's Signature <i>F.H.</i> Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 7
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TITLE Single crystal semiconductor manufacturing apparatus and method				
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